



Scalable Computing Software Laboratory Technical Report Department of Computer Science Illinois Institute of Technology

Reevaluating Memory Stall Time via Concurrent AMAT

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Dec 2, 2013

Technical Report No. IIT/CS-SCS2013-12

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ABSTRACT

Memory stall time is the CPU stall time due to memory references. Driven by the infamous memory-wall problem, memory stall time has become a prominent performance bottleneck of high-end computing systems. Intensive studies have been conducted in recent years to reduce memory stall time. However, most of them are developed based on the conventional average memory access time (AMAT) formulation, which has inherent limitations in characterizing concurrency and is not appropriate for modern out-of-order processors. Concurrent-AMAT (C-AMAT) is an extension of AMAT which considers data access concurrency in its formulation. In this research, we use the newly proposed C-AMAT formulation to reevaluate memory stall time. Our contribution is four-fold: first we derive the relationship between memory stall time, reduction; then simulation results are conducted to verify our theoretical findings; finally a summary is presented to discuss the possible research directions to reduce memory stall time via memory system concurrency and application memory access concurrency are formally analyzed and explored. Considering the importance of memory system concurrency and application memory access concurrency are formally analyzed and explored. Considering the importance of memory stall time, the new memory stall time formulation proposed in this study has the potential to be an effective tool for both future architecture and software design on various modern processors.

Categories and Subject Descriptors

D.3.3 [Computer Systems Organization]: Performance of Systems

General Terms

Performance, Algorithms, Theory, Measurement, Verification

Keywords

Memory stall time; memory wall; memory concurrency; concurrent average memory access time (C-AMAT); average memory access time (AMAT)